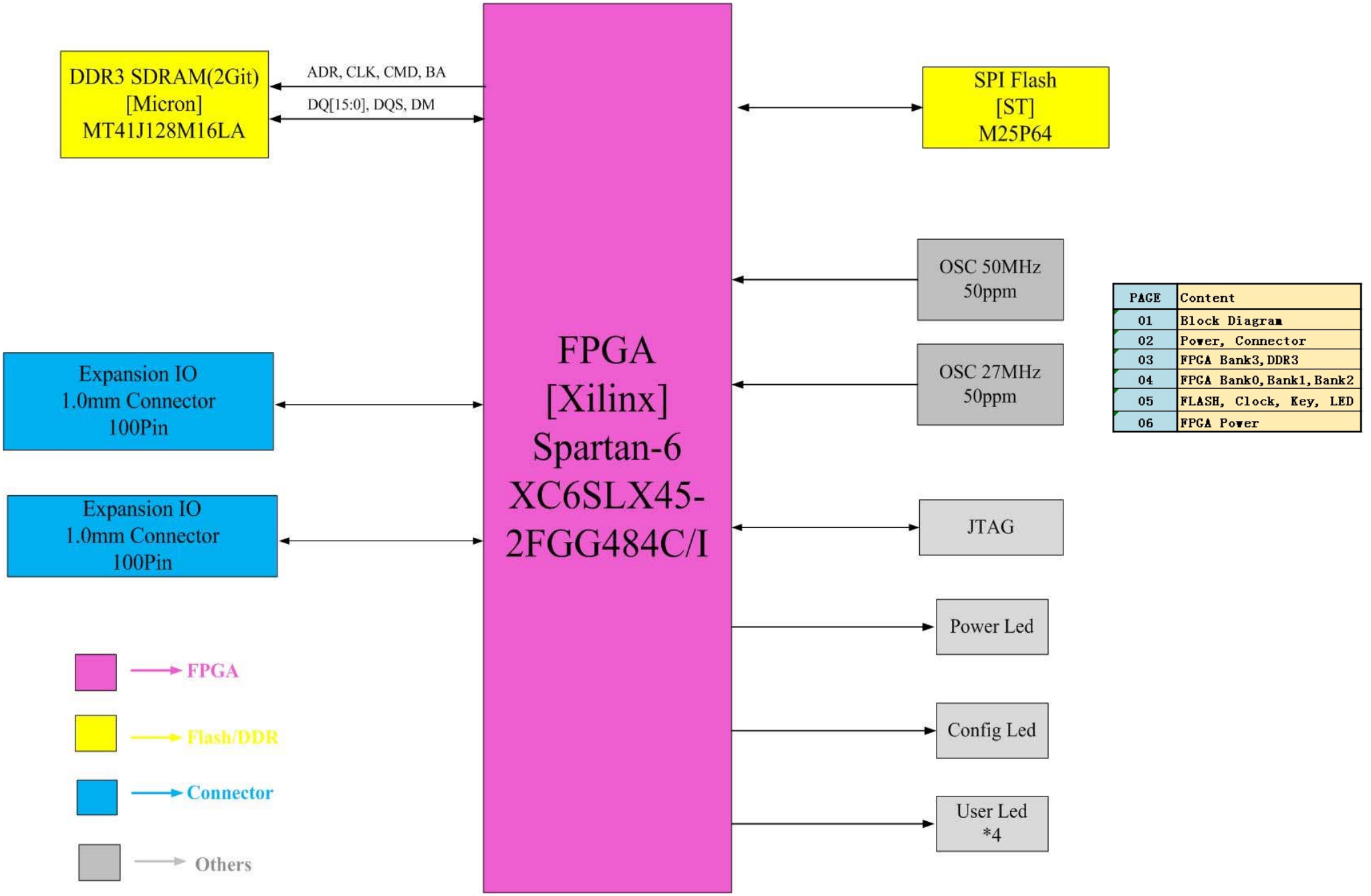

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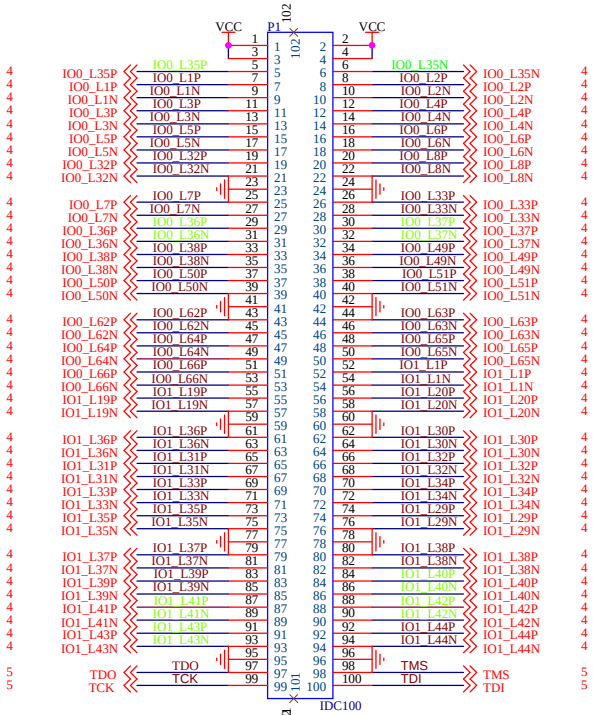
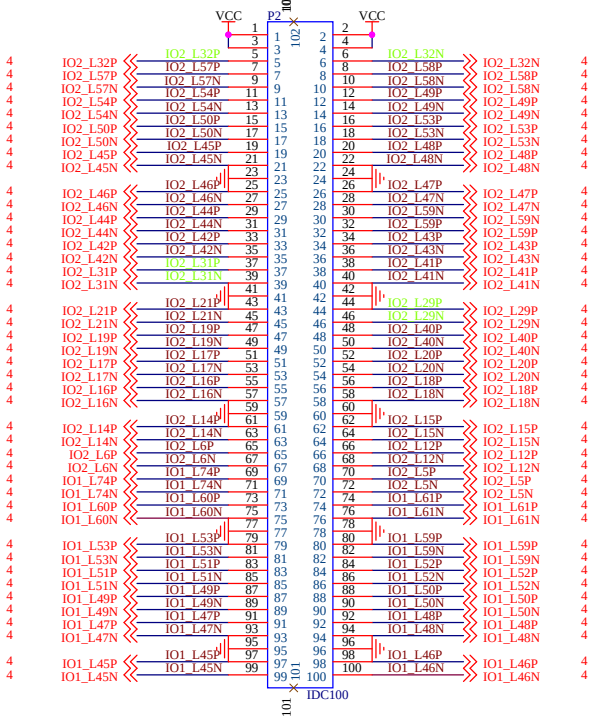
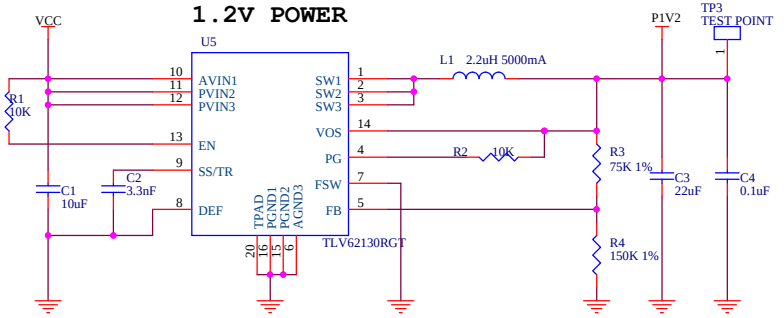
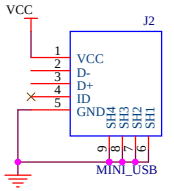
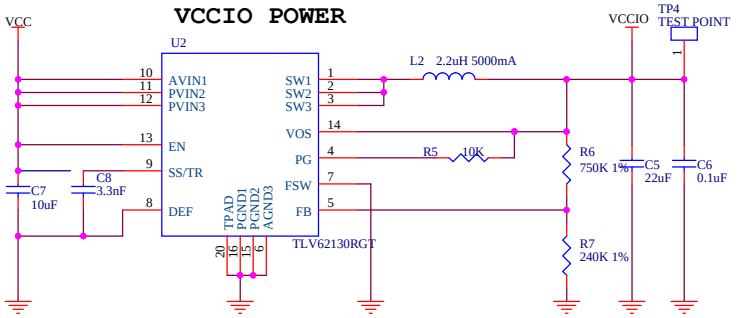
01	Block Diagram
02	Power, Connector
03	FPGA Bank3, DDR3
04	FPGA Bank0, Bank1, Bank2
05	FLASH, Clock, Key, LED
06	FPGA Power



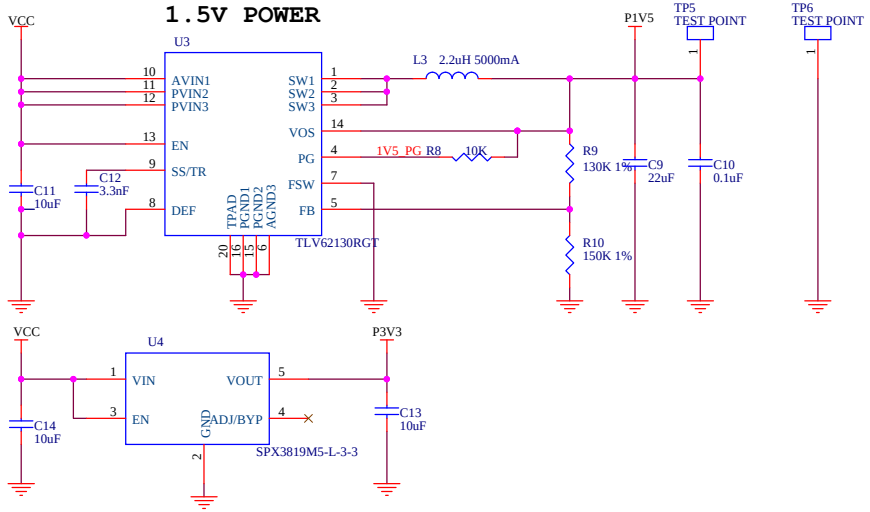








R6	R7	VCCIO Voltage
750K	240K	3.3V
510K	240K	2.5V



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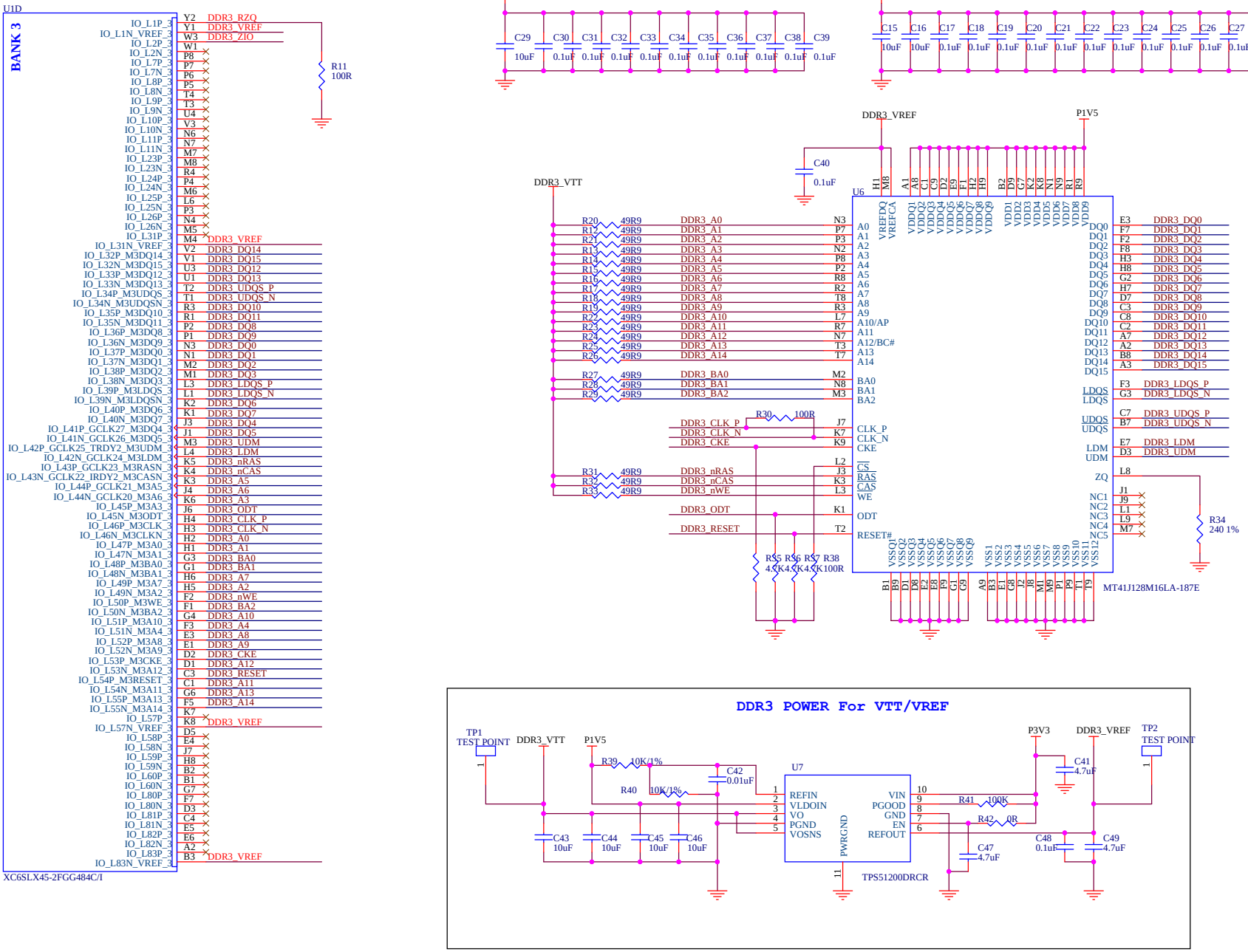

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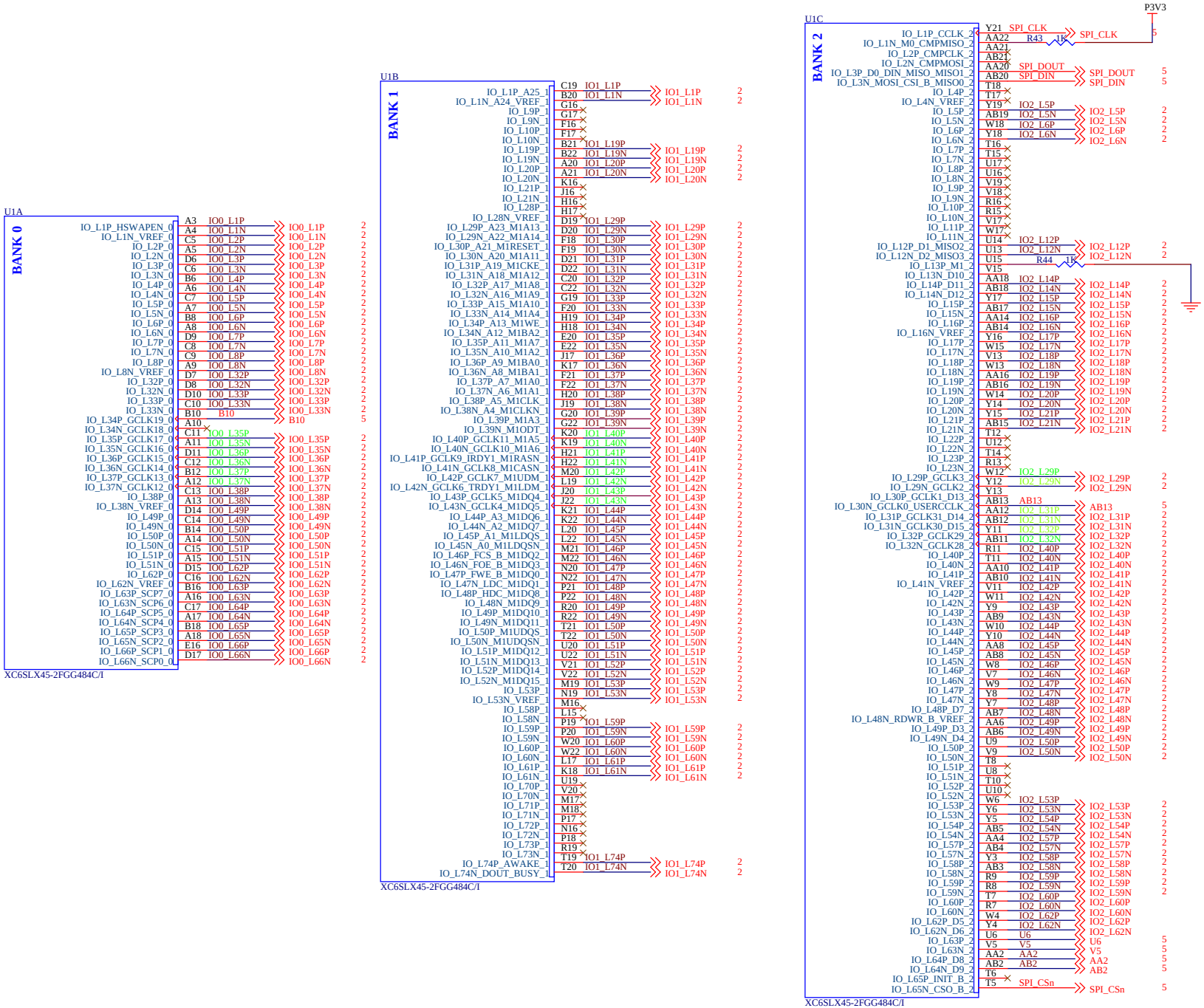
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IO_L66N_SCP0_0
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A4 IO0_L1N
C5 IO0_L2P
A5 IO0_L2N
D6 IO0_L3P
C6 IO0_L3N
B6 IO0_L4P
A6 IO0_L4N
C7 IO0_L5P
A7 IO0_L5N
B8 IO0_L6P
A8 IO0_L6N
D9 IO0_L7P
C8 IO0_L7N
C9 IO0_L8P
A9 IO0_L8N
D7 IO0_L32P
D8 IO0_L32N
D10 IO0_L33P
C10 IO0_L33N
B10 B10
A10
C11 IO0_L35P
A11 IO0_L35N
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C16 IO0_L62N
B16 IO0_L63P
A16 IO0_L63N
C17 IO0_L64P
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G17
F16
F17
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A21 IO1_L20N
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H16
H17
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AB21
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AB20 SPI_DIN
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W18 IO2_L6P
Y18 IO2_L6N
T16
T15
U17
U16
V19
V18
R16
R15
V17
W17
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U13 IO2_L12N
U15 R44 1K
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AB14 IO2_L16N
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W15 IO2_L17N
V13 IO2_L18P
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AB16 IO2_L19N
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T14
R13
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AB6 IO2_L49N
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U8
T10
U10
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AB4 IO2_L57N
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AB3 IO2_L58N
R9 IO2_L59P
R8 IO2_L59N
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R7 IO2_L60N
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Y4 IO2_L62N
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V5 V5
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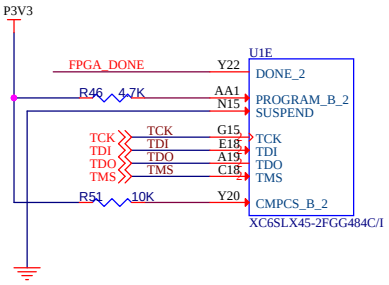

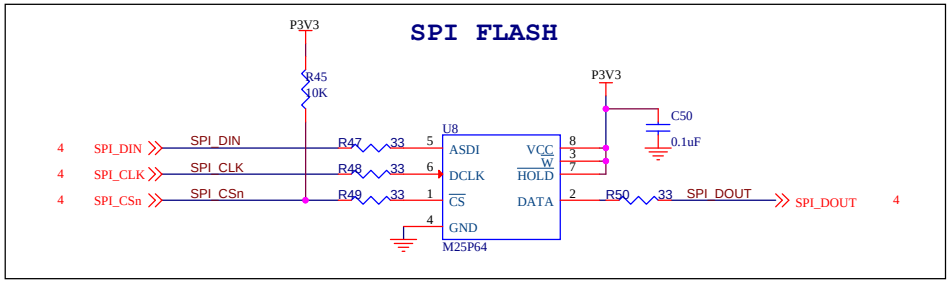

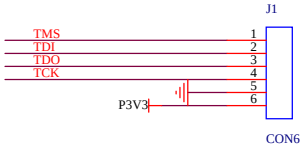

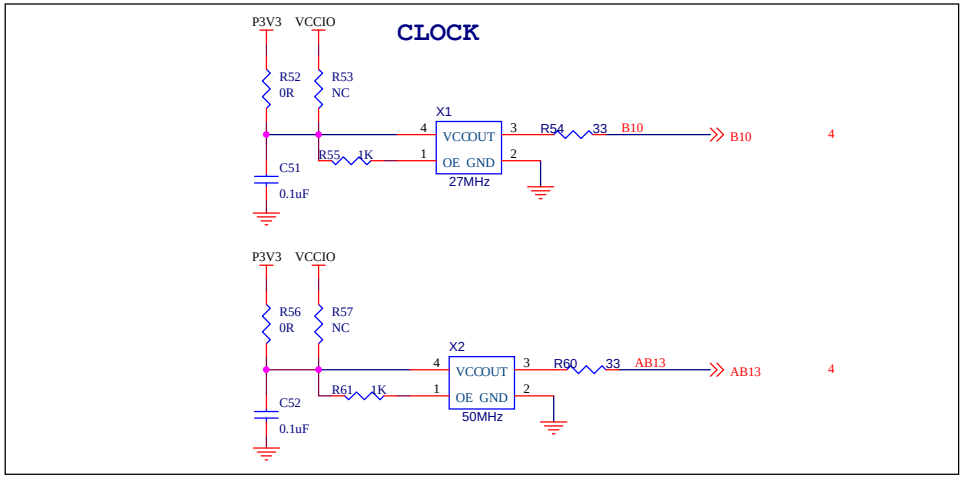

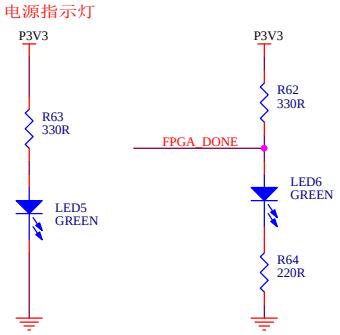

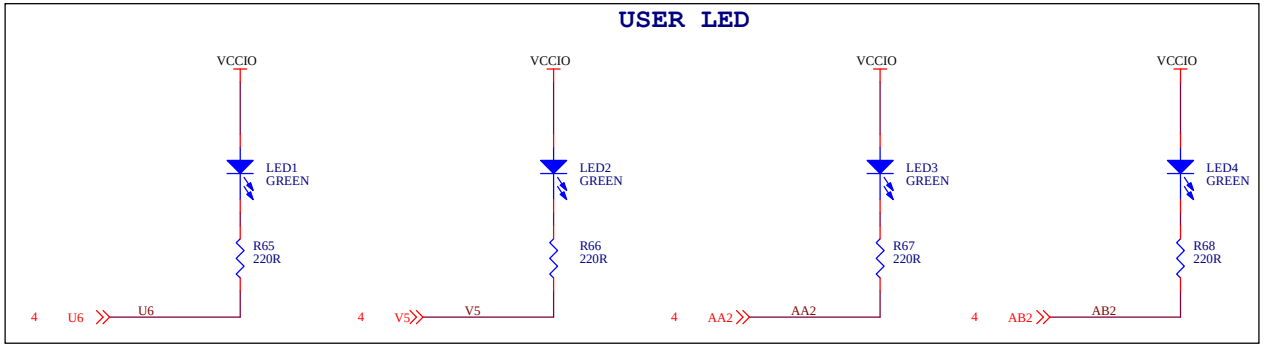

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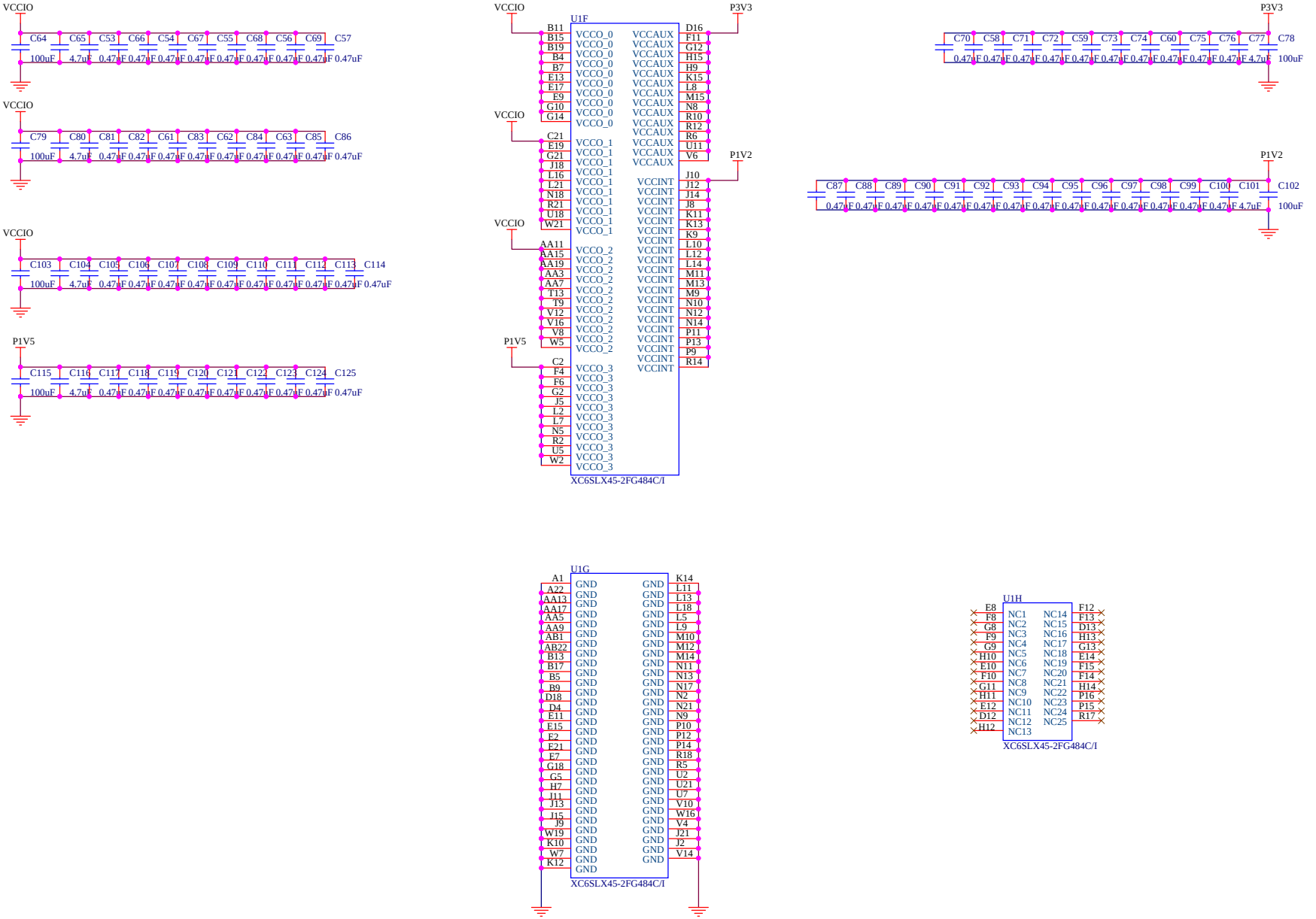

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